

# נוסחאון במיקרו-בקר 8051 לכיתה י"ג

סוג הבחינה: גמר לבתי-ספר לטכנאים ולהנדסאים  
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(15 עמודים)

אין להעביר את הנוסחאון  
לנבחן אחר

מקום למחברות נבחן

## נוסחאון בשפת ASM-51

ARITHMETIC OPERATIONS				DATA TRANSFER (cont.)			
Mnemonic	Description	Byte	Cyc	Mnemonic	Description	Byte	Cyc
ADD A,Rn	Add register to Accumulator	1	1	MOVC A,@A+DPTR	Move Code byte relative to DPTR to A	1	2
ADD A,direct	Add direct byte to Accumulator	2	1	MOVC A,@A+PC	Move Code byte relative to PC to A	1	2
ADD A,@Ri	Add indirect RAM to Accumulator	1	1	MOVX A,@Ri	Move External RAM (8-bit addr) to A	1	2
ADD A,#data	Add immediate data to Accumulator	2	1	MOVX A,DPTR	Move External RAM (16-bit addr) to A	1	2
ADDC A,Rn	Add register to Accumulator with Carry	1	1	MOVX @Ri,A	Move A to External RAM (8-bit addr)	1	2
ADDC A,direct	Add direct byte to A with Carry flag	2	1	MOVX @DPTR,A	Move A to External RAM (16-bit addr)	1	2
ADDC A,@Ri	Add indirect RAM to A with Carry flag	1	1	PUSH direct	Push direct byte onto stack	2	2
ADDC A,#data	Add immediate data to A with Carry flag	2	1	POP direct	Pop direct byte from stack	2	2
SUBB A,Rn	Subtract register from A with Borrow	1	1	XCH A,Rn	Exchange register with Accumulator	1	1
SUBB A,direct	Subtract direct byte from A with Borrow	2	1	XCH A,direct	Exchange direct byte with Accumulator	2	1
SUBB A,@Ri	Subtract indirect RAM from A w/Borrow	1	1	XCH A,@Ri	Exchange indirect RAM with A	1	1
SUBB A,#data	Subtract immed. data from A w/Borrow	2	1	XCHD A,@Ri	Exchange low-order Digit ind. RAM w/A	1	1
INC A	Increment Accumulator	1	1	<b>BOOLEAN VARIABLE MANIPULATION</b>			
INC Rn	Increment register	1	1	<b>Mnemonic</b>	<b>Description</b>	<b>Byte</b>	<b>Cyc</b>
INC direct	Increment direct byte	2	1	CLR C	Clear Carry flag	1	1
INC @Ri	Increment indirect RAM	1	1	CLR bit	Clear direct bit	2	1
DEC A	Decrement Accumulator	1	1	SETB C	Set Carry flag	1	1
DEC Rn	Decrement register	1	1	SETB bit	Set direct Bit	2	1
DEC direct	Decrement direct byte	2	1	CPL C	Complement Carry flag	1	1
DEC @Ri	Decrement indirect RAM	1	1	CPL bit	Complement direct bit	2	1
INC DPTR	Increment Data Pointer	1	2	ANL C,bit	AND direct bit to Carry flag	2	2
MUL AB	Multiply A & B	1	4	ANL C,/bit	AND complement of direct bit to Carry	2	2
DIV AB	Divide A by B	1	4	ORL C,bit	OR direct bit to Carry flag	2	2
DA A	Decimal Adjust Accumulator	1	1	ORL C,/bit	OR complement of direct bit to Carry	2	2
<b>LOGICAL OPERATION</b>				<b>Mnemonic</b>	<b>Description</b>	<b>Byte</b>	<b>Cyc</b>
ANL A,Rn	AND register to Accumulator	1	1	MOV C,bit	Move direct bit to Carry flag	2	1
ANL A,direct	AND direct byte to Accumulator	2	1	MOV bit,C	Move Carry flag to direct bit	2	2
ANL A,@Ri	AND indirect RAM to Accumulator	1	1	<b>PROGRAM AND MACHINE CONTROL</b>			
ANL A,#data	AND immediate data to Accumulator	2	1	<b>Mnemonic</b>	<b>Description</b>	<b>Byte</b>	<b>Cyc</b>
ANL direct,A	AND Accumulator to direct byte	2	1	ACALL addr11	Absolute Subroutine Call	2	2
ANL direct,#data	AND immediate data to direct byte	3	2	LCALL addr16	Long Subroutine Call	3	2
ORL A,Rn	OR register to Accumulator	1	1	RET	Return from subroutine	1	2
ORL A,direct	OR direct byte to Accumulator	2	1	RETI	Return from interrupt	1	2
ORL A,@Ri	OR indirect RAM to Accumulator	1	1	AJMP addr11	Absolute Jump	2	2
ORL A,#data	OR immediate data to Accumulator	2	1	LJMP addr16	Long Jump	3	2
ORL direct,A	OR Accumulator to direct byte	2	1	SJMP rel	Short Jump (relative addr)	2	2
ORL direct,#data	OR immediate data to direct byte	3	2	JMP @A+DPTR	Jump indirect relative to the DPTR	1	2
XRL A,Rn	Exclusive-OR register to Accumulator	1	1	JZ rel	Jump if Accumulator is Zero	2	2
XRL A,direct	Exclusive-OR direct byte to Accumulator	2	1	JNZ rel	Jump if Accumulator is Not Zero	2	2
XRL A,@Ri	Exclusive-OR indirect RAM to A	1	1	JC rel	Jump if Carry flag is set	2	2
XRL A,#data	Exclusive-OR immediate data to A	2	1	JNC rel	Jump if No Carry flag	2	2
XRL direct,A	Exclusive-OR Accumulator to direct byte	2	1	JB bit,rel	Jump if direct Bit set	3	2
XRL direct,#data	Exclusive-OR immediate data to direct	3	2	JNB bit,rel	Jump if direct Bit Not set	3	2
CLR A	Clear Accumulator	1	1	JBC bit,rel	Jump if direct Bit is set & Clear bit	3	2
CPL A	Complement Accumulator	1	1	CJNE A,direct,rel	Compare direct to A & Jump if Not Equal	3	2
RL A	Rotate Accumulator Left	1	1	CJNE A,#data,rel	Comp. immed. to A & Jump if Not Equal	3	2
RLC A	Rotate A Left through the Carry flag	1	1	CJNE Rn,#data,rel	Comp. immed. to reg & Jump if Not Equal	3	2
RR A	Rotate Accumulator Right	1	1	CJNE @Ri,#data,rel	Comp. immed. to ind. & Jump if Not Equal	3	2
RRC A	Rotate A Right through Carry flag	1	1	Rn,rel	Decrement register & Jump if Not Zero	2	2
SWAP A	Swap nibbles within the Accumulator	1	1	DJNZ direct,rel	Decrement direct & Jump if Not Zero	3	2
<b>DATA TRANSFER</b>				DJNZ direct,rel	Decrement direct & Jump if Not Zero	3	2
<b>Mnemonic</b>	<b>Description</b>	<b>Byte</b>	<b>Cyc</b>	NO	No operation	1	1
MOV A,Rn	Move register to Accumulator	1	1	Notes on data addressing modes:			
MOV A,direct	Move direct byte to Accumulator	2	1	Rn	-Working register R0-R7		
MOV A,@Ri	Move indirect RAM to Accumulator	1	1	direct	-128 internal RAM locations, any I/O port, control or status register		
MOV A,#data	Move immediate data to Accumulator	2	1	@Ri	-Indirect internal RAM location addressed by register R0 or R1		
MOV Rn,A	Move Accumulator to register	1	1	#data	-8-bit constant included in instruction		
MOV Rn,direct	Move direct byte to register	2	2	#data16	-16-bit constant included as bytes 2 & 3 of instruction		
MOV Rn,#data	Move immediate data to register	2	1	bit	-128 software flags, any I/O pin, control or status bit		
MOV direct,A	Move Accumulator to direct byte	2	1	<b>Notes on program addressing modes:</b>			
MOV direct,Rn	Move register to direct byte	2	2	addr16	-Destination address for LCALL & LJMP may be anywhere within the 64-Kilobyte program memory address space.		
MOV direct,direct	Move direct byte to direct	3	2	addr11	-Destination address for ACALL & AJMP will be within the same 2-Kilobyte page of program memory as the first byte of the following instruction.		
MOV direct,@Ri	Move indirect RAM to direct byte	2	2	rel	-SJMP and conditional jumps include an 8-bit offset byte. Range is +127/-128 bytes relative to first byte of the following instruction.		
MOV direct,#data	Move immediate data to direct byte	3	2				
MOV @Ri,A	Move Accumulator to indirect RAM	1	1				
MOV @Ri,direct	Move direct byte to indirect RAM	2	2				
MOV @Ri,#data	Move immediate data to indirect RAM	2	1				
MOVDPTR,#data	Load Data Pointer with a 16-bit constant	3	2				

### INSTRUCTIONS THAT AFFECT FLAG SETTINGS'

INSTRUCTION	FLAG			INSTRUCTION	FLAG		
	C	OV	AC		C	OV	AC
ADD	X	X	X	CLR C	0		
ADDC	X	X	X	CPL C	X		
SUBB	X	X	X	ANL C,bit	X		
MUL	0	X		ANL C,/bit	X		
DIV	0	X		ORL C,bit	X		
DA	X			ORL C,/bit	X		
RRC	X			MOV C,bit	X		
RLC	X			CJNE	X		
SETB C	1						

## Special Function Registers

### P3-Alternate Special Functions of Port 3

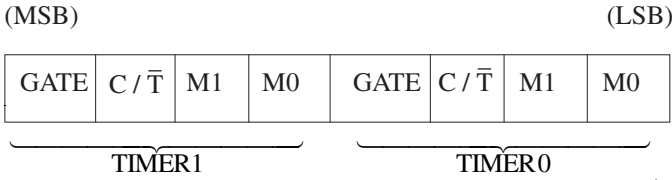
(MSB)

(LSB)

RD	WR	T1	T0	INT1	INT0	TXD	RXD
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Symbol	Position	Name and Significance	Symbol	Position	Name and Significance
RD	P3.7	Read data control output. Active low pulse generated by hardware when external data memory is read.	INT1	P3.3	Interrupt 1 input pin. Low-level or falling-edge triggered.
			INT0	P3.2	Interrupt 0 input pin. Low-level or falling-edge triggered.
WR	P3.6	Write data control output. Active low pulse generated by hardware when external data memory is written.	TXD	P3.1	Transmit Data pin for serial port in UART mode. Clock output in shift register mode.
T1	P3.5	Timer/counter 1 external input or test pin.	RXD	P3.0	Receive Data pin for serial port in UART mode. Data I/O pin in shift register mode.
T0	P3.4	Timer/counter 0 external input or test pin.			

### TMOD-Timer/Counter Mode Register



		M1	M0	
		0	0	<b>Operating Mode</b> MCS-48 Timer "TLx" serves as five bit prescaler.
		0	1	16-bit timer/counter. "THx" and "TLx" are cascaded; there is no prescaler.
		1	0	8-bit auto-reload timer/ counter. "THx" holds a value which is to be reloaded into "TLx" each time it overflows.
<b>GATE</b>	Gating control. When set, Timer/ counter "x" is enabled only while "INTx" pin is high and "TRx" control bit is set. When cleared, timer/counter is enabled whenever "TRx" control bit is set.	1	1	(Timer 0) TL0 is an eight-bit timer/ counter controlled by the standard Timer 0 control bits. TH0 is an eight-bit timer only controlled by Timer 1 control bits.
<b>C / <math>\bar{T}</math></b>	Timer or Counter Selector. Cleared for Timer operation (input from internal system clock). Set for Counter operation (input from "Tx" input pin).	1	1	(Timer 1) Timer/ counter 1 stopped.

## TCON-Timer/Counter Control/Status Register

(MSB)

(LSB)

TF1	TR1	TF0	TR0	IE1	IT1	IE0	IT0
-----	-----	-----	-----	-----	-----	-----	-----

Symbol	Position	Name and Significance	Symbol	Position	Name and Significance
TF1	TCON.7	Timer 1 overflow Flag. Set by hardware on timer/ counter overflow. Cleared when interrupt processed.	IE1	TCON.3	Interrupt 1 Edge flag. Set by hardware when external interrupt edge detected. Cleared when interrupt processed.
TR1	TCON.6	Timer 1 Run control bit. Set/cleared by software to turn timer/counter on/off.	IT1	TCON.2	Interrupt 1 Type control bit. Set/cleared by software to specify falling edge/low level triggered external interrupts.
TF0	TCON.5	Timer 0 overflow Flag. Set by hardware on timer/ counter overflow. Cleared when interrupt processed.	IE0	TCON.1	Interrupt 0 Edge flag. Set by hardware when external interrupt edge detected. Cleared when interrupt processed.
TR0	TCON.4	Timer 0 Run control bit. Set/cleared by software to turn timer/counter on/off.	IT0	TCON.0	Interrupt 0 Type control bit. Set/cleared by software to specify falling edge/low level triggered external interrupts.

## SCON-Serial Port Control/Status Register

(MSB)

(LSB)

SM0	SM1	SM2	REN	TB8	RB8	TI	RI
-----	-----	-----	-----	-----	-----	----	----

Symbol	Position	Name and Significance	Symbol	Position	Name and Significance
SM0	SCON.7	Serial port Mode control bit 0. Set/cleared by software (see note).	RB8	SCON.2	Receive Bit 8. Set/cleared by hardware to indicate state of ninth data bit received.
SM1	SCON.6	Serial port Mode control bit 1. Set/cleared by software (see note).	TI	SCON.1	Transmit Interrupt flag. Set by hardware when byte transmitted. Cleared by software after servicing.
SM2	SCON.5	Serial port Mode control bit 2. Set by software to disable reception of frames for which bit 8 is zero.	RI	SCON.0	Receive Interrupt flag. Set by hardware when byte received. Cleared by software after servicing.
REN	SCON.4	Receiver Enable control bit. Set/cleared by software to enable/disable serial data reception.			
TB8	SCON.3	Transmit Bit 8. Set/cleared by hardware to determine state of ninth data bit transmitted in 9-bit UART mode.			

**Note** — the state of (SM0, SM1) selects:  
 (0,0) — Shift register I/O expansion.  
 (0,1) — 8 bit UART, variable data rate.  
 (1,0) — 9 bit UART, fixed data rate.  
 (1,1) — 9 bit UART, variable data rate.

## IE-Interrupt Enable Register

(MSB)

(LSB)

EA	—	—	ES	ET1	EX1	ET0	EX0
----	---	---	----	-----	-----	-----	-----

Symbol	Position	Name and Significance	Symbol	Position	Name and Significance
EA	IE.7	Enable All control bit. Cleared by software to disable all interrupts, independent of the state of IE.4-IE.0.	EX1	IE.2	Enable External interrupt 1 control bit. Set/cleared by software to enable/disable interrupts from INT1.
—	IE.6	(reserved)	ET0	IE.1	Enable Timer 0 control bit. Set/cleared by software to enable/disable interrupts from timer/counter 0.
—	IE.5	(reserved)			
ES	IE.4	Enable Serial port control bit. Set/cleared by software to enable/disable interrupts from TI or RI flags.	EX0	IE.0	Enable External interrupt 0 control bit. Set/cleared by software to enable/disable interrupts from INT0.
ET1	IE.3	Enable Timer 1 control bit. Set/cleared by software to enable/disable interrupts from timer/counter 1.			

### IP-Interrupt Priority Control Register

(MSB)	—	—	—	PS	PT1	PX1	PT0	PX0	(LSB)
<b>Symbol</b>	<b>Position</b>	<b>Name and Significance</b>			<b>Symbol</b>	<b>Position</b>	<b>Name and Significance</b>		
—	IP.7	(reserved)			PX1	IP.2	External interrupt 1		
—	IP.6	(reserved)					Priority control bit. Set/cleared by software to specify high/low priority interrupts for INT1.		
—	IP.5	(reserved)							
PS	IP.4	Serial port Priority control bit. Set/cleared by software to specify high/low priority interrupts for Serial port.			PT0	IP.1	Timer 0 Priority control bit. Set/cleared by software to specify high/low priority interrupts for timer/counter 0.		
PT1	IP.3	Timer 1 Priority control bit. Set/cleared by software to specify high/low priority interrupts for timer/counter 1.			PX0	IP.0	External interrupt 0 Priority control bit. Set/cleared by software to specify high/low priority interrupts for INT0.		

Register	Address	Function	Interrupt Source	Service Routine Starting Address
P0	80H*	Port 0	(Reset)	0000H
SP	81H	Stack Pointer	External 0	0003H
DPL	82H	Data Pointer (Low)	Timer/Counter 0	000BH
DPH	83H	Data Pointer (High)	External 1	0013H
TCON	88H*	Timer register	Timer/Counter 1	001BH
TMOD	89H	Timer Mode register	Serial Port	0023H
TL0	8AH	Timer 0 Low byte		
TL1	8BH	Timer 1 Low byte		
TH0	8CH	Timer 0 High byte		
TH1	8DH	Timer 1 High byte		
P1	90H*	Port 1		
SCON	98H*	Serial Port Control register		
SBUF	99H	Serial Port data Buffer		
P2	0A0H*	Port 2		
IE	0A8H*	Interrupt Enable register		
P3	0B0H*	Port 3		
IP	0B8H*	Interrupt Priority register		
PSW	0D0H*	Program Status Word		
ACC	0E0H*	Accumulator (direct address)		
B	0F0H*	B register		

\*=bit addressable register

## נוסחאון בשפת C של המיקרו־בקר 8051

נוסחאון זה מתאים למהדר Keil uVision3. חלקים ממנו מתאימים גם למהדרים אחרים.

### Data Types (טיפוסי נתונים)

Name	Description	תאור	Size	Range
bit	One Bit	ביט בודד	1 bit	0 to 1
char	Character or small integer	תו בודד או בית	1 byte	-128 to 127
unsigned char	Unsigned small integer	בית אחד ללא סימן	1 byte	0 to 255
int	Integer	מספר שלם	2 bytes	-32768 to 32767
unsigned int	Unsigned integer	מספר שלם ללא סימן	2 bytes	0 to 65535
long	Long integer	מספר שלם ארוך	4 bytes	-2147483648 to 2147483647
unsigned long	Unsigned long integer	מספר שלם ארוך ללא סימן	4 bytes	0 to 4294967295
float	Floating point number	מספר ממשי	4 bytes	+/- 1.175494E-38 to +/- 3.402823E+38
sbit	Special Bit	ביט מיוחד	1 bit	0 to 1
sfr	8 bits special Function Registers	בית מיוחד	1 byte	0 to 255
sfr16	16 bits special Function Registers	בית כפול מיוחד	2 bytes	0 to 65535

דוגמאות:

```
unsigned char a;
int b, c;
sfr P1=0x90;
sbit P1_7 = 0x97;
```



**Memory Areas (אזורי הזיכרון)**

Name	Description	Example
data	places the variable in directly addressable RAM in the micro core	unsigned int data dnum;
xdata	places the variable in external RAM	unsigned char xdata xnum_at_0x8000; *
idata	places the variable in indirectly addressable memory within the micro core	int idata inum;
code	places the variable in program memory	unsigned char code cnum=0xAA;

\* \_at\_ – places the variable in absolute address.

**Preprocessor-directives (הנחיות לקדם-מהדר)**

Description	Syntax	Example
macro definitions	#define identifier replacement	#define LED P 1_7

**Operators (אופרטורים)**

Description	תאור	Operator
Assignment	השמה	=

**Initialization of variables (אתחול משתנים)**

```
unsigned char d=0;
d=75; // decimal number
d=0x4B; // hexadecimal number
```

identifier - מזהה ; replacement - תחליף

**Arithmetic Operators (אופרטורים חשבוניים)**

Description	תאור	Operator
addition	חיבור	+
subtraction	חיסור	-
multiplication	כפל	*
division	חילוק	/
modulo	שארית	%

**Relational and equality operators (אופרטורים להשוואה ויחסים)**

Description	תאור	Operator
Equal to	שווה	==
Not equal to	שונה	!=
Greater than	גדול מ־	>
Less than	קטן מ־	<
Greater than or equal to	גדול מ־ / שווה	>=
Less than or equal to	קטן מ־ / שווה	<=

**Logical operators (אופרטורים לוגיים בין ביטויים)**

Description	תאור	Operator
NOT	היפוך	!
AND	וגם	&&
OR	או	

**Bitwise Operators (אופרטורים על סיביות)**

Description	תאור	Operator
AND	וגם	&
Inclusive OR	או כולל	
Exclusive OR (XOR)	או מוציא	^
Byte inversion	היפוך בית	~
Bit inversion	היפוך סיבית	!
Shift Left	הזזה שמאלה	<<
Shift Right	הזזה ימינה	>>

**Conditional Structures (מבני בקרה – משפטי תנאי)**

Description	Syntax	Example
if	<pre>if (condition) {     statements ; }</pre>	<pre>if (d == 100) {     P1 = 0xFF; }</pre>
if .. else	<pre>if (condition)     statement ; else     statement ;</pre>	<pre>if (d == 100)     P1 = 0xFF; else     P1=0;</pre>
if .. else if .. else	<pre>if (condition)     statement ; else if (condition)     statement ; else     statement ;</pre>	<pre>if (d &gt; 0)     P1=4; else if (d &lt; 0)     P1=2; else     P1=1;</pre>

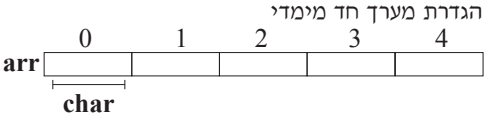


הצהרה – statement ; תנאי – condition

**Iteration Structures (מבני בקרה - לולאות)**

Description	Syntax	Example
while loop	<pre>while (expression) {     statements ; }</pre>	<pre>while (n&gt;0) {     P1=n;     n--; }</pre>
do-while loop	<pre>do {     statements ; } while (condition);</pre>	<pre>do {     n=P1; } while (n != 0);</pre>
for loop	<pre>for (initialization; condition; increase) {     statements ; }</pre>	<pre>for (i=0; i&lt;10; i++) {     P1=i; }</pre>

condition - תנאי ; statement - הצהרה ; initialization - אתחול ; increase - קידום

**Arrays (מערכים)**

Description	Syntax	Example
<p>הגדרת מערך חד מימדי</p> 	type name [number of elements];	char arr[5];
<p>אתחול והצבת ערכים במערך</p> 	type name [number of elements] = {value1,..valueN};	char arr[5] = {3,5,7,-1, 14};
<p>הגדרת מערך דו מימדי</p> 	type name [number of elements] [number of elements];	char arr[3][5];

elements – פרטים ; value – ערך

**Structure of a program (מבנה כללי של תוכנית)**

```
#include <8051.h> //including headers for the SFR definitions
                    of your microcontroller

void main()
{
    while(1)
    {
        //Your code
    }
}
```

**Functions (פונקציות)**

Description	Syntax	Example
Functions with no type and no argument	<pre>void name (void) {     statements; }</pre>	<pre>void OutData(void) {     P1=0xAB; } void main() {     OutData(); }</pre>
Functions with no type	<pre>void name ( parameter1, parameter2, ...) {     statements; }</pre>	<pre>void OutData(unsigned char a, unsigned char b) {     P0=a;     P1=b; } void main() {     OutData(0xF,0xF0); }</pre>
Functions with type and argument	<pre>type name ( parameter1, parameter2, ...) {     statements; }</pre>	<pre>unsigned char InOutData(unsigned char a) {     P1=a;     return P0; } void main() {     unsigned char r;     r = InOutData(63);     P2=r; }</pre>

parameter – טיעון ; argument – טיפוס ; type – הצהרה ; statement – ערך המועבר לפונקציה

**Interrupt Service Routines (שגרות לטיפול בפסיקות)**

Interrupt Number	Description	Address
0	External INT 0	0003h
1	Timer 0	000Bh
2	External INT 1	0013h
3	Timer 1	001Bh
4	Serial port	0023h

דוגמה:

```
void int2sub () interrupt 2
{
    // Interrupt code
}
```